

O/L Series Intelligent Off-Line Reader Instructions

4130-32 Series Module Operation

DIPSWITCH functions:

Switch 1

Switch ON = Mass-Programs all Cards VALID or VOID, depending on the setting of Switch #2.

This erases any previous manual Card Deck Programming of any card.

This also re-synchronizes the APB memory, if APB is in use.

This switch MUST be turned OFF to enable Normal operation.

Switch 2

Switch ON = Programs all cards Valid. Switch OFF = All cards Void.

Note: At the very first application of power, all cards will be pre-programmed Valid or Void (depending on the state of Switch #2), regardless of the state of Switch #1. After that, the battery will maintain the card memory until Mass-Programmed or individually Programmed with a Card Programming Deck.

Switch 3 through 6: Not Applicable

Switch 7

Switch ON = Enable Timed APB re-synchronization at 15 minute intervals (starting with last Module reset, or last Card Mass Programming). IMPORTANT: Switch #8 must also be ON.

Switch 8

Switch ON = Enable Hard APB @ module level.

Switch 9: Not Applicable

Switch 10

Relay Diode interconnection Switch off = Relays are totally independent of each other

Switch ON= Relay 2 “linked” to Relay 1 through diode

Anti-Passback-Back:

For dual-reader modules, Reader 1 & Reader 2 are APB opposites.

The APB memory is automatically re-synchronized after any module reset, or whenever Dip switch #1 is ON (Card Mass-Programming).

Valet-Exempt cards must be encoded with a site code that is numerically ONE (decimal) higher than the normal cards for a given system.

If a Valet card is used, the module will then skip APB detection, and only check the Hand-Program memory for Valid or Void status of the memory number.